What is claimed is:

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- 1. A video driving module for multiple monitors, comprising:
- a CRT controller; and
- a plurality of converters;
- wherein the CRT controller generates a video signal and the video signal is divided into a plurality of equal parts, each of the parts being associated with one of the converters.
 - 2. The video driving module as in claim 1, wherein the CRT controller converts a plurality of parts of the image signal into the plurality of video signals.
 - 3. The video driving module as in claim 2, further comprising a video memory to store the image signal.
 - 4. The video driving module as in claim 1, wherein each of the video signals is a digital signal.
- 5. The video driving module as in claim 1, wherein the CRT controller has a graphics engine.
 - 6. The video driving module as in claim 1, wherein the CRT controller generates a vertical/horizontal sync signal to the monitors.
- 7. The video driving module as in claim 1, wherein the converter is a digital-to-analog converter (DAC).
 - 8. A motherboard for multiple monitors, comprising:
 - a chipset for outputting a plurality of image signals;
 - a CRT controller for converting the plurality of image signals into a plurality of video signals; and

- a plurality of converters for converting the video signals into signals adapted for the monitors and outputting the signals to monitors.
- 9. The motherboard as in claim 8, wherein the CRT controller generates a vertical/horizontal synchronization signal to the monitors.
- 5 10. The motherboard as in claim 8, wherein the converter is a digital-to-analog converter (DAC).
 - 11. The motherboard as in claim 8, further comprising a video memory to store the image signal.
 - 12. A method for driving multiple monitors, a plurality of monitors being driven by a CRT controller and a plurality of converters, the method comprising following steps:

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the CRT controller processing a plurality of image signals into a plurality of video signals;

sending the plurality of video signals to the plurality of converters for converting the video signals into signals adapted for the monitors; and

sending the signals adapted for the monitors to the monitors.

- 13. The method for driving multiple monitors as in claim 12, further comprising a step of storing the image signals in a video memory.
- 14. The method for driving multiple monitors as in claim 12, wherein the20 converters convert the video signals into RGB analog signals.
 - 15. The method for driving multiple monitors as in claim 12, wherein the CRT controller generates a vertical/horizontal synchronization signal to the monitors.